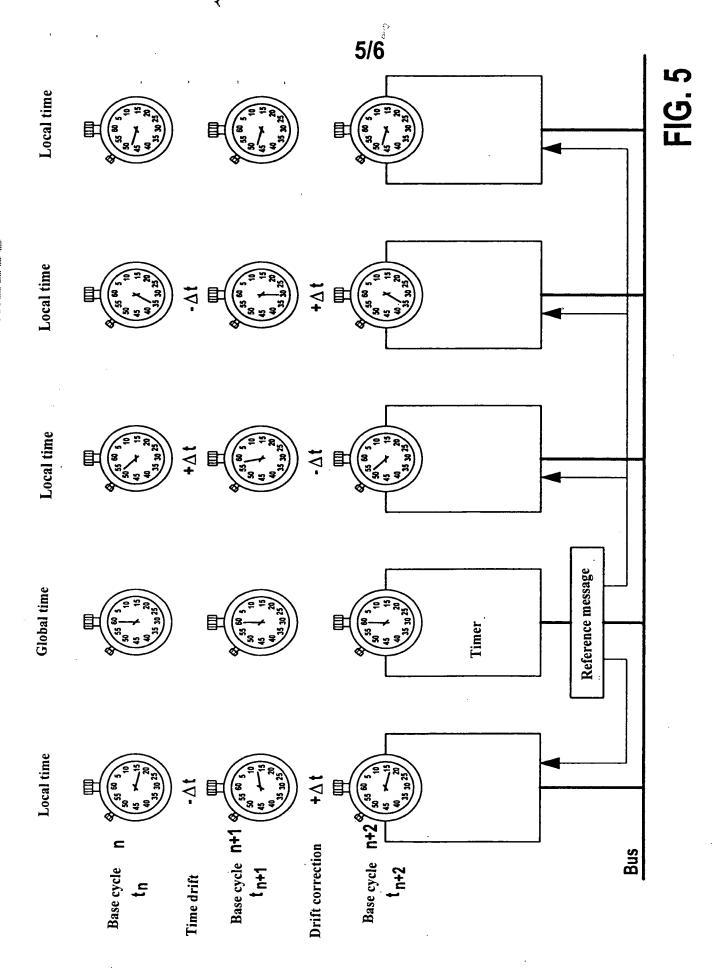


							4/6	•					
FIG. 4		Timing window	ZF5a	405 B	411	417	423   C	429 B	435 ¢	441	447 C		
Base cycle BZ	(Line in the matrix)	Timing window	ZF4a	404 Å	410 A 4	416 A	422 Å /	428 Å	434 A	440 A	446 A		
		Timing window	ZF3a	Arbitrating		<b>Q</b>		Arbitrating	L	a		<u> </u>	
		Timing   window	ZF2a	402 E 403	408 E 409	414 415	420 E 421	426 E 427	432 E 433	438 E 439	444 E 445		
		Timing window	ZF1a	401 Å	407 Å	413 A	419 A	425 Å	431 A	437 A	443 A		
		Timing window (Column in the matrix) ZFRN (ZFO)		Time-reference message		. L							
Start cycle				BZ0a	BZ1a	S BZZa	rix) BZ3a	erall c: (Mati	BZ5a	BZ6a —	BZ7a		

A..F 

⊈ Transmission groups
Arbitrating 

⊈ reserved timing windows for arbitrating messages



Clock

**Drift correction** FIG. 6 (Block diagram) Start of Frame **End of Frame** Time-reference message from global timer Receive time-reference message Time-reference marker Local Buffer register time\_ base Take over Local offset new Reference marker new; FIFO FIFO Reference marker old Local offset old With respect to global **△ Offset** new **FIFO** time Reference time-reference △ Offset old message def. 0 Take over △ Offset  $\Delta$  ( $\Delta$  Offset) 1.B.→ Counter **Basic correction** Drift-correction-value register 2. Message 0..3 Take over Fine correction -3...n. Message def. 0 clock (load) Result Hardware Reset Local time interval Instantaneous drift-Time-reference message Preceding sign correction value = Correction period **Enable** Reload + Addition to the residual 1 pulse 1 pulse Clock Output Comparator additional suppress generator **Z < K Enable**